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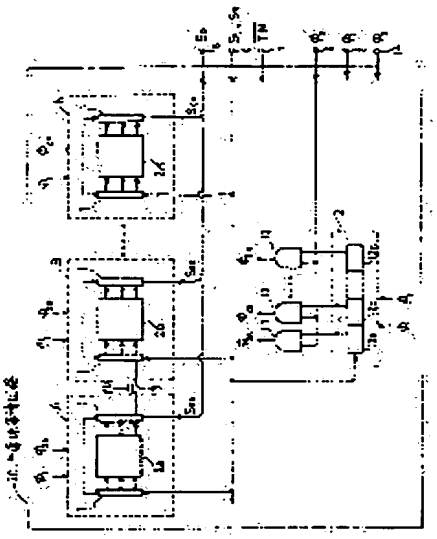
## (54) SEMICONDUCTOR INTEGRATED CIRCUIT

(57)Abstract:
PURPOSE: To unnecessitate an increase of terminals for receiving a clock signal from outside, by giving the clock signal selectively to a shift register specified by a specification signal.
CONSTITUTION: First a scan path 1 to give a clock signal ϕ2 to a register is specified. Concretely, a specification signal Ss is given to a serial input terminal 4 and a register 12 stores the signal in response to clock signals ϕ1 and ϕ3. When the signal ϕ2 is given only to the scan path 1 connected to a circuit block 2a, for instance, the specification signal Ss is given so that a shift register 12a outputs a voltage of high level. By giving the signals ϕ1 and ϕ2 after the specification, the signal ϕ2 is given to the specified scan path 1 and a test data signal St is inputted. Since the scan path 1 connected to another circuit block is not given the clock signal, only the desired scan path 1 can be made to operate selectively.





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